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06/18/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10064171	FILING DATE 06/18/2002	CLASS 257	SUBCLASS 413	GAU 2845	EXAMINER
**APPLICANTS: Adkisson James; Agnello Paul; Ballantine Arne; Divakaruni Rama; Jones Erin; Rankin Jed; 2811					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/711,725 11/13/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		BUR919990299	
Verified and Acknowledged Examiners's initials					
TITLE : Double gate trench transistor					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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